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A Signal Integrity Engineer's Companion Real-Time Test and Measurement and Design Simulation Geoff Lawday David Ireland Greg Edlund Foreword by Chris Edwards, Editor, IET Electronics Systems and Software magazine Prentice Hall Modern Semiconductor Design Series Prentice Hall Signal Integrity Library Use Real-World Test and Measurement Techniques to Systematically Eliminate Signal Integrity Problems This is the industry's most comprehensive, authoritative, and practical guide to modern Signal Integrity (SI) test and measurement for high-speed digital designs. Three of the field's leading experts guide you through systematically detecting, observing, analyzing, and rectifying both modern logic signal defects and embedded system malfunctions. The authors cover the entire life cycle of embedded system design from specification and simulation onward, illuminating key techniques and concepts with easy-to-understand illustrations. Writing for all electrical engineers, signal integrity engineers, and chip designers, the authors show how to use real-time test and measurement to address today's increasingly difficult interoperability and compliance requirements. They also present detailed, start-to-finish case studies that walk you through commonly encountered design challenges, including ensuring that interfaces consistently operate with positive timing margins without incurring excessive cost; calculating total jitter budgets; and managing complex tradeoffs in high-speed serial interface design. Coverage includes Understanding the complex signal integrity issues that arise in today's high-speed designs Learning how eye diagrams, automated compliance tests, and signal analysis measurements can help you identify and solve SI problems Reviewing the electrical characteristics of today's most widely used CMOS IO circuits Performing signal path analyses based on intuitive Time-Domain Reflectometry (TDR) techniques Achieving more accurate real-time signal measurements and avoiding probe problems and artifacts Utilizing digital oscilloscopes and logic analyzers to make accurate measurements in high-frequency environments Simulating real-world signals that stress digital circuits and expose SI faults Accurately measuring jitter and other RF parameters in wireless applications About the Authors: Dr. Geoff Lawday is Tektronix Professor in Measurement at Buckinghamshire New University, England. He delivers courses in signal integrity engineering and high performance bus systems at the University Tektronix laboratory, and presents signal integrity seminars throughout Europe on behalf of Tektronix. David Ireland, European and Asian design and manufacturing marketing manager for Tektronix, has more than 30 years of experience in test and measurement. He writes regularly on signal integrity for leading technical journals. Greg Edlund, Senior Engineer, IBM Global Engineering Solutions division, has participated in development and testing for ten high-performance computing platforms. He authored Timing Analysis and Simulation for Signal Integrity Engineers (Prentice Hall).

The #1 guide to signal integrity, updated with all-new coverage of power integrity, high-speed serial links, and more \* \* \* Up-to-the-minute comprehensive guidance: everything engineers need to know to understand and design for signal integrity. \* Authored by world-renowned signal integrity trainer, educator, and columnist Eric Bogatin. \* Focuses on intuitive understanding, practical tools, and engineering discipline - not theoretical derivation or mathematical rigor. Today's marketplace demands faster devices and systems that deliver more functionality and longer life in smaller packaging. Signal Integrity - Simplified, Second Edition is the first book to bring together all the up-to-the-minute techniques designers need to overcome all of those challenges. Renowned expert Eric Bogatin thoroughly reviews the root causes of all four families of signal integrity problems, and shows how to design them out early in the design cycle. Drawing on his experience teaching 5,000+ engineers, he illuminates signal integrity, physical design, bandwidth, inductance, and impedance; presents practical tools for solving signal integrity problems; and offers specific design guidelines and solutions. In this edition, Bogatin adds extensive coverage of power integrity and high speed serial links: topics at the forefront of signal integrity design. Three new chapters address: \* \* \* Designing power delivery networks to support high-speed signal processing. \* Using 4-Port S-parameters, the emerging standard for describing interconnects in high speed serial links. \* Working with today's measurement and simulation tools and technologies

Providing a complete introduction to the state-of-the-art in high-speed digital testing with automated test equipment (ATE), this practical resource is the first book focus exclusively on this increasingly important topic. Featuring clear examples, this one-stop reference covers all critical aspects of the subject, from high-speed digital basics, ATE instrumentation for digital applications, and test and measurements, to production testing, support instrumentation and test fixture design. This in-depth volume also discusses at advanced ATE topics, such as multiplexing of ATE pin channels and testing of high-speed bi-directional interfaces with fly-by approaches.

Within the past few decades, information technologies have been evolving at a tremendous rate, causing profound changes to our world and our ways of life. In particular, fiber optics has been playing an increasingly crucial role within the telecommunication revolution. Not only most long-distance links are fiber based, but optical fibers are increasingly approaching the individual end users, providing wide bandwidth links to support all kinds of data-intensive applications such as video, voice, and data services. As an engineering discipline, fiber optics is both fascinating and challenging. Fiber optics is an area that incorporates elements from a wide range of techno- gies including optics, microelectronics, quantum electronics, semiconductors, and networking. As a result of rapid changes in almost all of these areas, fiber optics is a fast evolving field. Therefore, the need for up-to-date texts that address this growing field from an interdisciplinary perspective persists. This book presents an overview of fiber optics from a practical, engineering perspective. Therefore, in addition to topics such as lasers, detectors, and optical fibers, several topics related to electronic circuits that generate, detect, and process the optical signals are covered. In other words, this book attempts to present fiber optics not so much in terms of "optics" but more from the perspective of an engineering field within "optoelectronics.

The Number 1 VLSI Design Guide...Now Fully Updated for IP-Based Design and the Newest Technologies Modern VLSI Design, Fourth Edition, offers authoritative, up-to-the-minute guidance for the entire VLSI design process—from architecture and logic design through layout and packaging. Wayne Wolf has systematically updated his award-winning book for today's newest technologies and highest-value design techniques. Wolf introduces powerful new IP-based design techniques at all three levels: gates, subsystems, and architecture. He presents deeper coverage of logic design fundamentals, clocking and timing, and much more. No other VLSI guide presents as much up-to-date information for maximizing performance, minimizing power utilization, and achieving rapid design turnarounds.

Embedded Systems: A Contemporary Design Tool, Second Edition Embedded systems are one of the foundational elements of today's evolving and growing computer technology. From operating our cars, managing our smart phones, cleaning our homes, or cooking our meals, the special computers we call embedded systems are quietly and unobtrusively making our lives easier, safer, and more connected. While working in increasingly challenging environments, embedded systems give us the ability to put increasing amounts of capability into ever-smaller and more powerful devices. Embedded Systems: A Contemporary Design Tool, Second Edition introduces you to the theoretical hardware and software foundations of these systems and expands into the areas of signal integrity, system security, low power, and hardware-software co-design. The text builds upon earlier material to show you how to apply reliable, robust solutions to a wide range of applications operating in today's often challenging environments. Taking the user's problem and needs as your starting point, you will explore each of the key theoretical and practical issues to consider when designing an application in today's world. Author James Peckol walks you through the formal hardware and software development process covering: Breaking the problem down into major functional blocks; Planning the digital and software architecture of the system; Utilizing the hardware and software co-design process; Designing the physical world interface to external analog and digital signals; Addressing security issues as an integral part of the design process; Managing signal integrity problems and reducing power demands in contemporary systems; Debugging and testing throughout the design and development cycle; Improving performance. Stressing the importance of security, safety, and reliability in the design and development of embedded systems and providing a balanced treatment of both the hardware and the software aspects, Embedded Systems: A Contemporary Design Tool, Second Edition gives you the tools for creating embedded designs that solve contemporary real-world challenges.

A unique, practical approach to the design of high-speed digital circuit boards The demand for ever-faster digital circuit designs is beginning to render the circuit theory used by engineers ineffective. Digital Circuit Boards presents an alternative to the circuit theory approach, emphasizing energy flow rather than just signal interconnection to explain logic circuit behavior. The book shows how treating design in terms of transmission lines will ensure that the logic will function, addressing both storage and movement of electrical energy on these lines. It covers transmission lines in all forms to illustrate how trace geometry defines where the signals can travel, then goes on to examine transmission lines as energy sources, the true nature of decoupling, types of resonances, ground bounce, cross talk, and more. Providing designers with the tools they need to lay out digital circuit boards for fast logic and to get designs working the first time around, Digital Circuit Boards: Reviews in simple terms the basic physics necessary to understand fast logic design Debunks the idea that electrical conductors carry power and signals, showing that signal travels in the spaces, not the traces, of circuit boards Explains logic circuit behavior through real-time analysis involving the fields and waves that carry signal and energy Provides new information on how ground/power planes work Outlines a software program for solving energy flow in complex networks

Master the usage of s-parameters in signal integrity applications and gain full understanding of your simulation and measurement environment with this rigorous and practical guide. Solve specific signal integrity problems including calculation of the s-parameters of a network, linear simulation of circuits, de-embedding, and virtual probing, all with expert guidance. Learn about the interconnectedness of s-parameters, frequency responses, filters, and waveforms. This invaluable resource for signal integrity engineers is supplemented with the open-source software SignalIntegrity, a Python package for scripting solutions to signal integrity problems.

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